

Iir::Cascade

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graph BT; A[Iir::ChebyshevII::HighShelfBase] --> B[Iir::PoleFilterBase< AnalogLowShelf >]; B --> C[Iir::PoleFilterBase2]; C --> D[Iir::Cascade];
```

Iir::PoleFilterBase2

Iir::PoleFilterBase< AnalogLowShelf >

Iir::ChebyshevII::HighShelfBase